

FIG 1

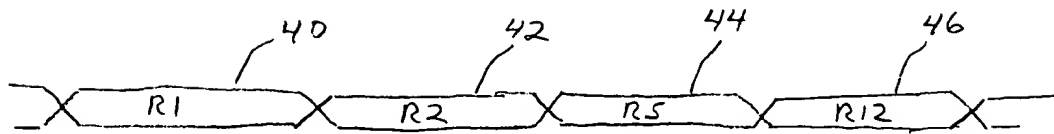


FIG 2

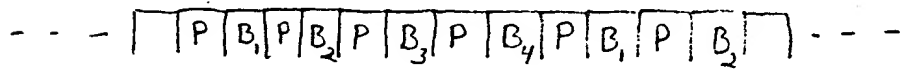


FIG 3

90  
CURRENT SYS STATE  
(RATE IN PRIOR MSG)

92

94

96

98

PRIOR MSG	1 Bit msg. (UP TO 2 MSGS)	2 Bit msg. (UP TO 4 MSGS)	3 Bit msg. (UP TO 8 MSGS)	4 Bit msg. (UP TO 16 MSGS)
R1	R1/R2	R3/R4/R5/R6	R7/./R14	refresh
R2	R2/R3	R1/R4/R5/R6	R7/./R14	refresh
R3	R3/R4	R1/R2/R5/R6	R7/./R14	refresh
R4	R4/R5	R2/R3/R6/R7	R8/./R14/R1	refresh
R5	R5/R6	R3/R4/R7/R8	R9/./R14/R1/R2	refresh
R6	R6/R7	R4/R5/R8/R9	R10/./R14/R1/./R3	refresh
R7	R7/R8	R5/R6/R9/R10	R11/./R14/R1/./R4	refresh
R8	R8/R9	R6/R7/R10/R11	R12/./R14/R1/./R5	refresh
R9	R9/R10	R7/R8/R11/R12	R13/R14/R1/./R6	refresh
R10	R10/R11	R8/R9/R12/R13	R14/R1/./R7	refresh
R11	R11/R12	R9/R10/R13/R14	R1/./R8	refresh
R12	R12/R13	R9/R10/R11/R14	R1/./R8	refresh
R13	R13/R14	R9/R10/R11/R12	R1/./R8	refresh
R14	R14/R13	R9/R10/R11/R12	R1/./R8	refresh

FIG 4

CURRENT SYS STATE  
(RATE IN PRIOR MSG)

PRIOR MSG

	1 Bit msg.	2 Bit msg.	3 Bit msg.	4 Bit msg.
R4	R4/R5	R2/R3/R6/R7	R8/./R14/R1	refresh
	0/1	00/01/10/11	0001---/110/111	0001/0010---/1110

FIG 5

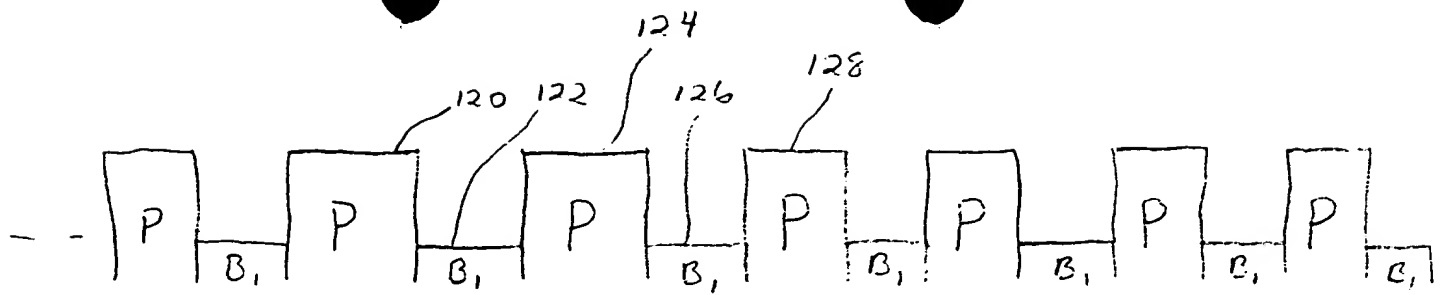


FIG 6A

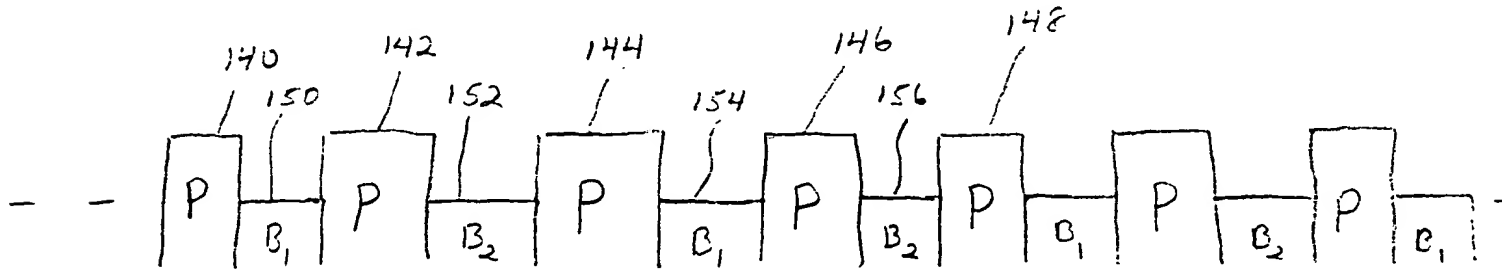


FIG 6B

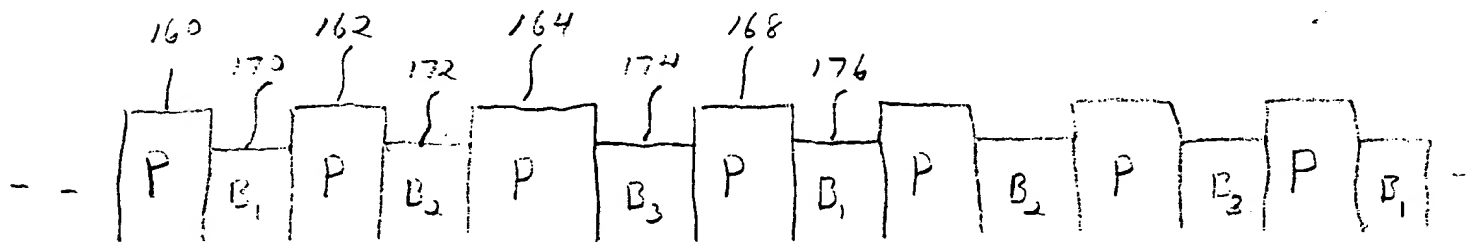
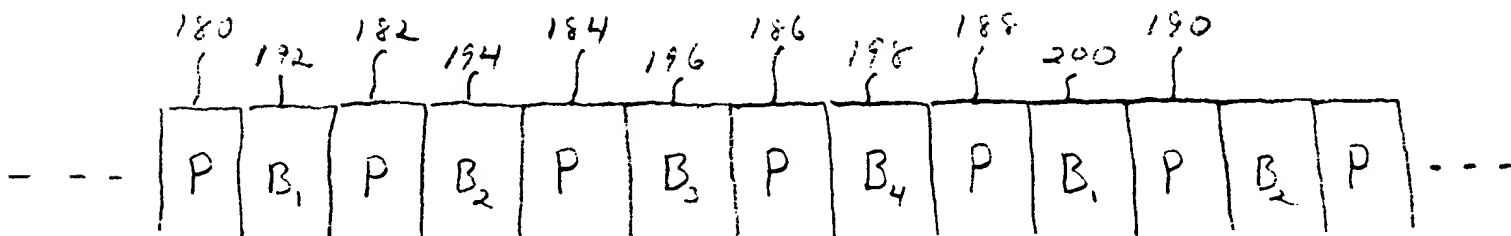
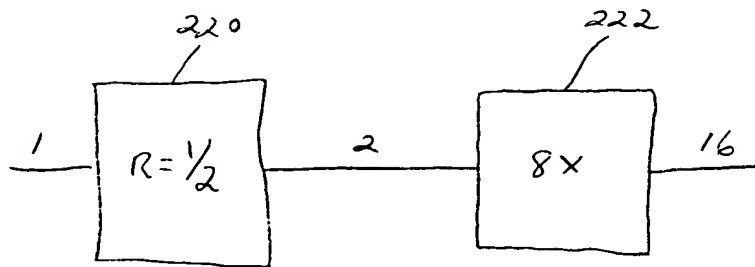


FIG 6C

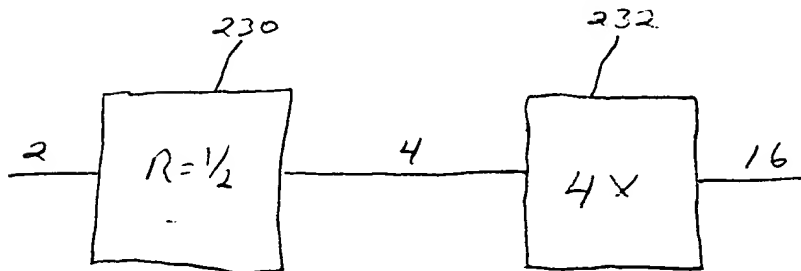


F16 GD



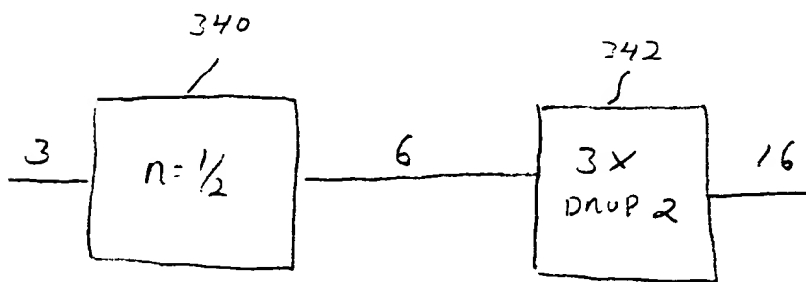
25% POWER

FIG 7A



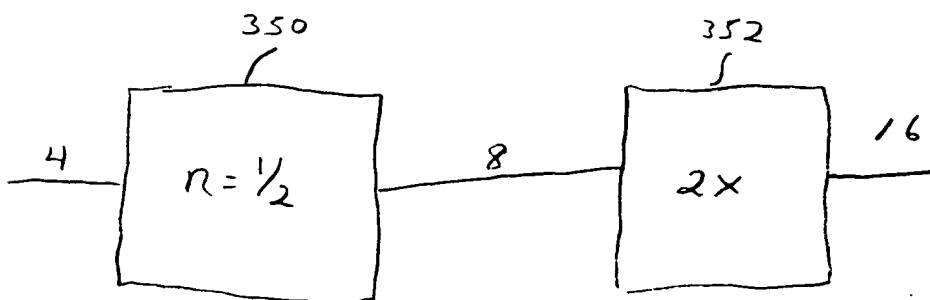
50% POWER

FIG 7B



75% POWER

FIG 7C



100% POWER

FIG 7D